

# Junction Field Effect Transistors (JFETs)

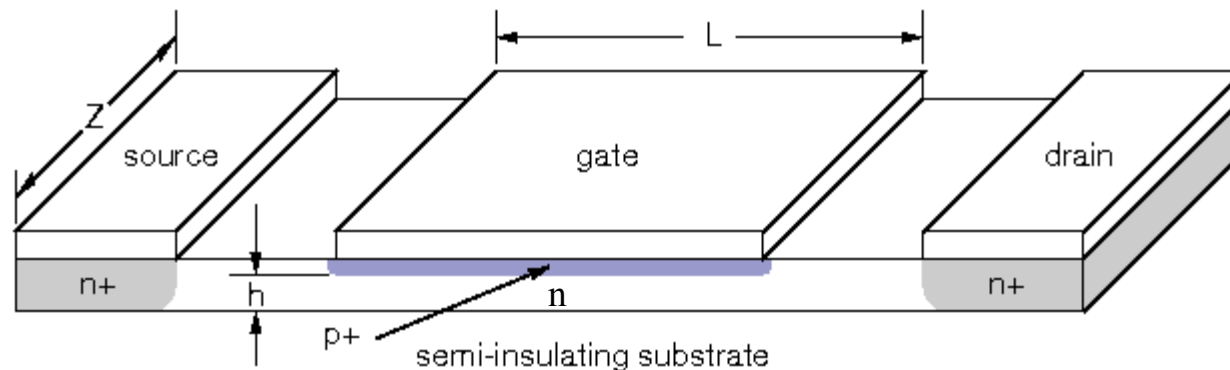
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# JFETs - MESFETs - MODFETs

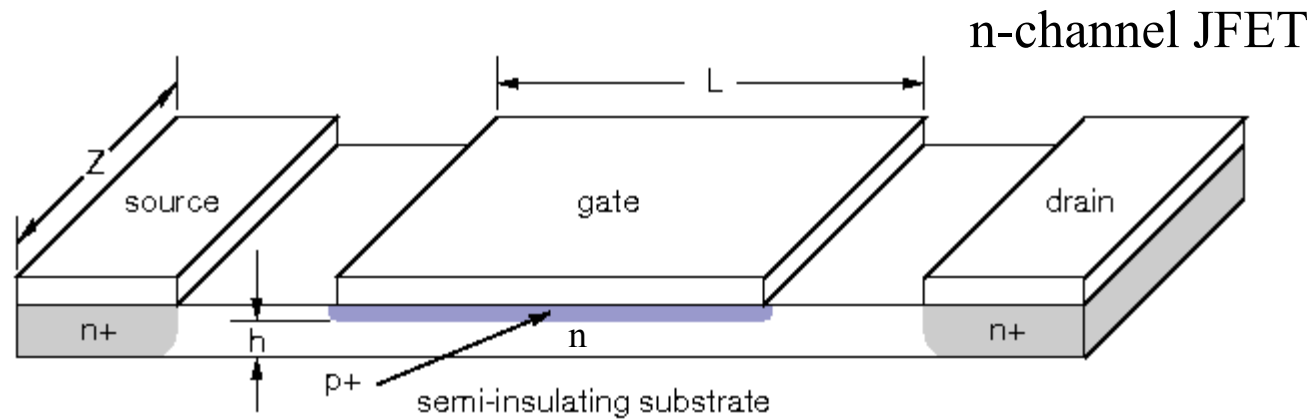
Junction Field Effect Transistors (JFET)

Metal-Semiconductor Field Effect Transistors (MESFET)

Modulation Doped Field Effect Transistors (MODFET)



# JFET



For  $N_A \gg N_D$

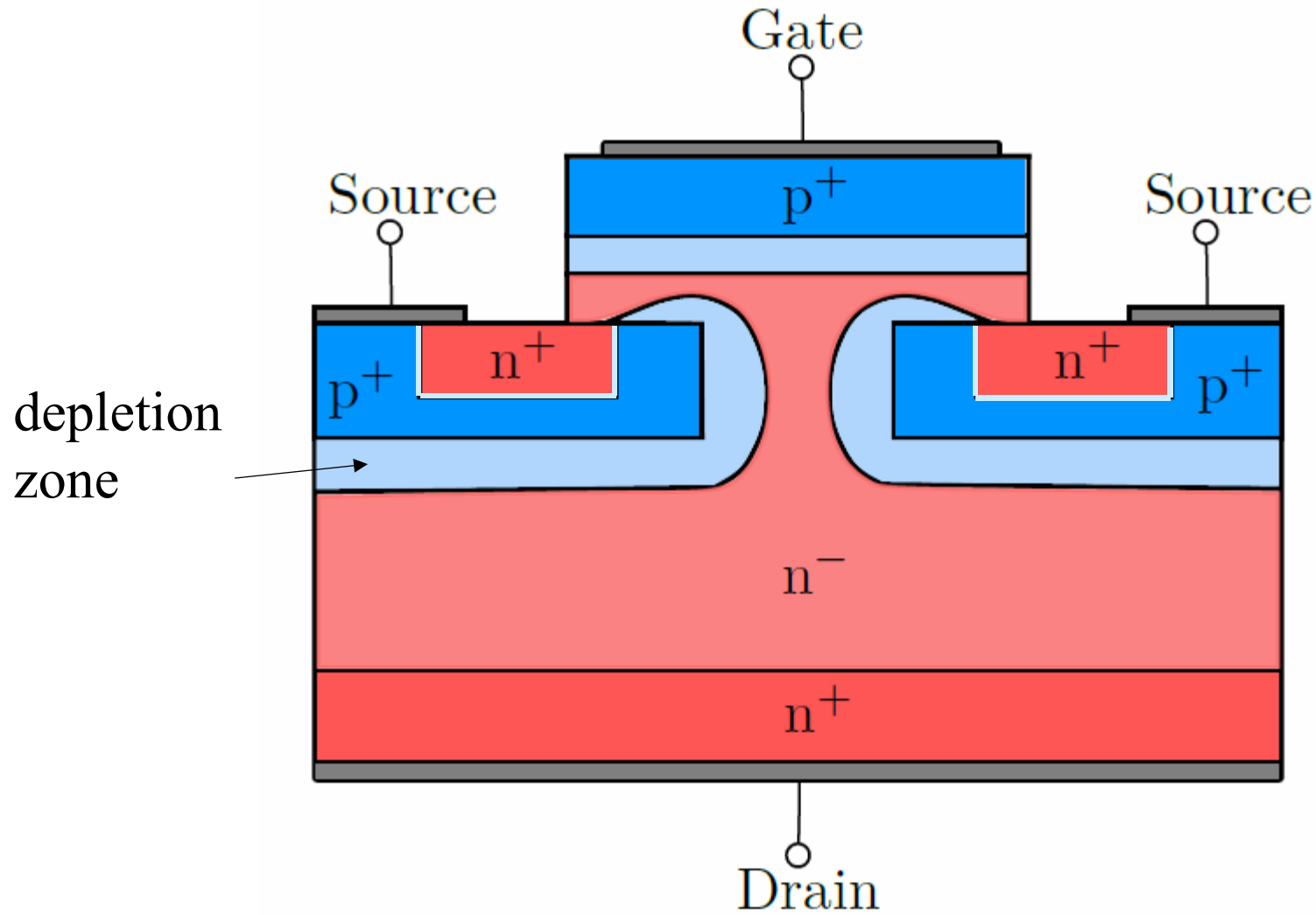
$$x_n = \sqrt{\frac{2\varepsilon(V_{bi} - V)}{eN_D}}$$

Depletion mode  $h > x_n = \sqrt{\frac{2\varepsilon V_{bi}}{eN_D}}$  conducting at  $V_g = 0$

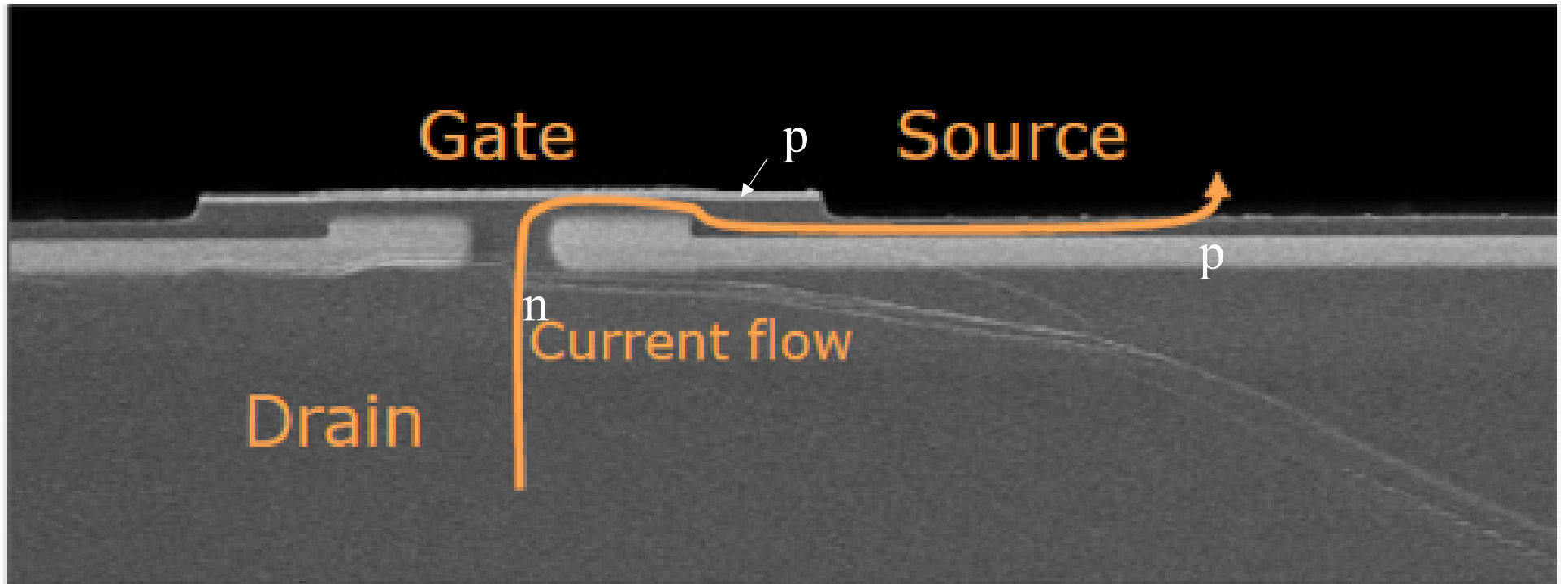
Enhancement mode  $h < x_n = \sqrt{\frac{2\varepsilon V_{bi}}{eN_D}}$  nonconducting at  $V_g = 0$

# n-channel (power) JFET

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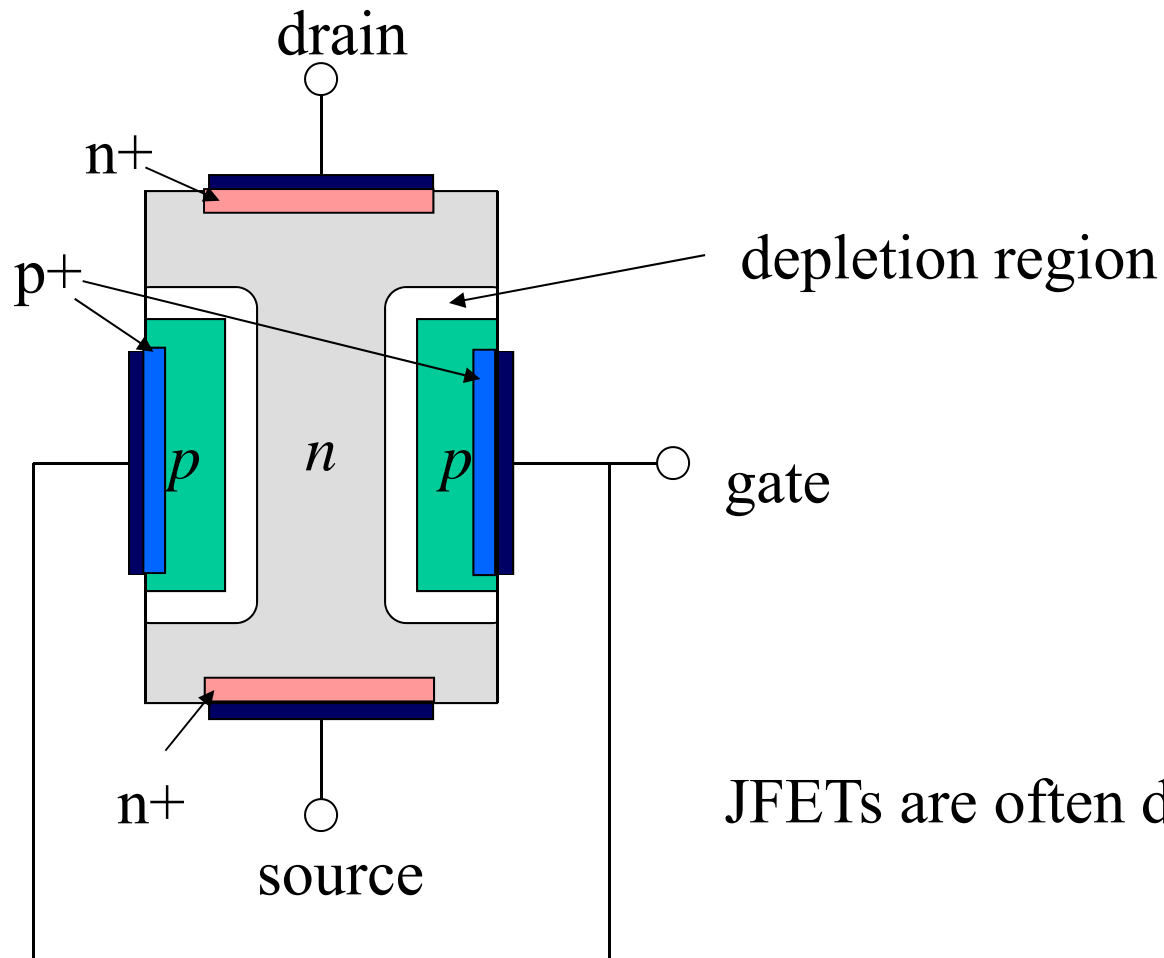


# Power SiC JFET



# n-channel JFET

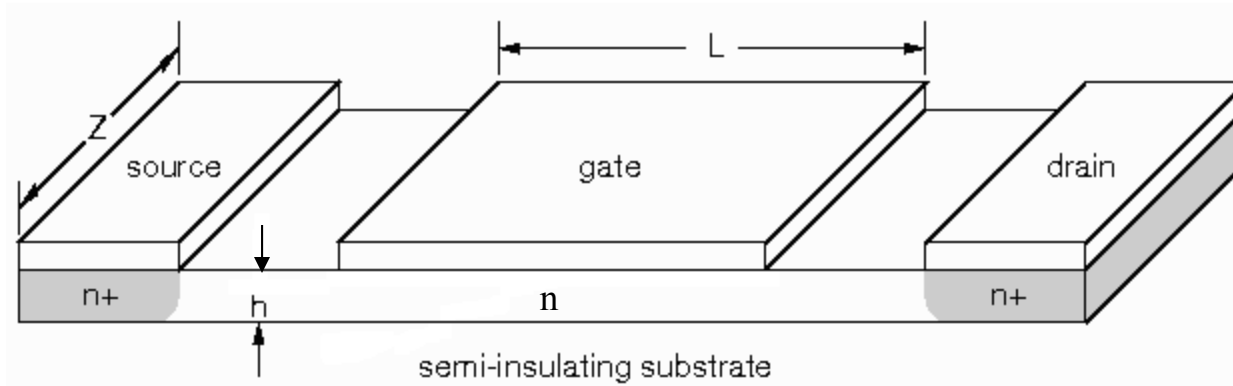
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JFETs are often discrete devices

# MESFET

## Metal-Semiconductor Field Effect Transistors

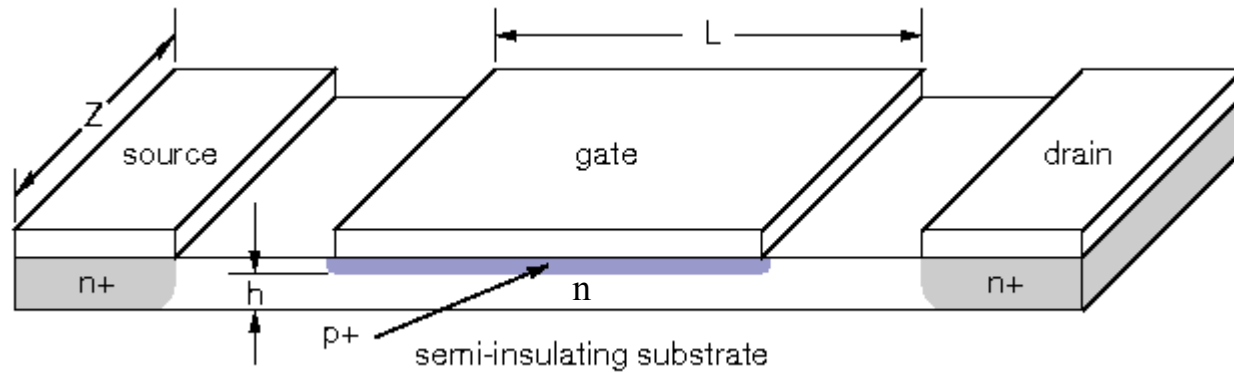


Depletion layer created by Schottky barrier

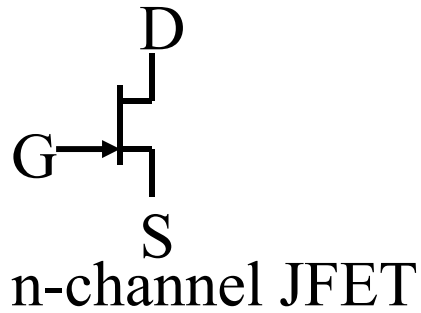
$$x_n = \sqrt{\frac{2\epsilon(V_{bi} - V)}{eN_D}}$$

Fast transistors can be realized in n-channel GaAs, however GaAs has a low hole mobility making p-channel devices slower.

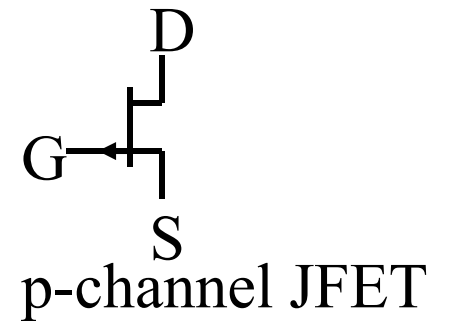
# JFET



n-channel JFET



$$x_n = \sqrt{\frac{2\epsilon(V_{bi} - V)}{eN_D}}$$



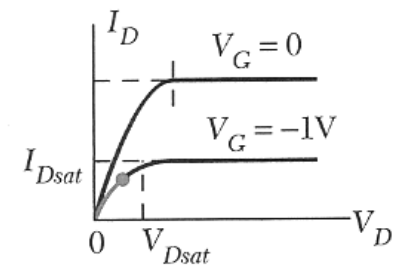
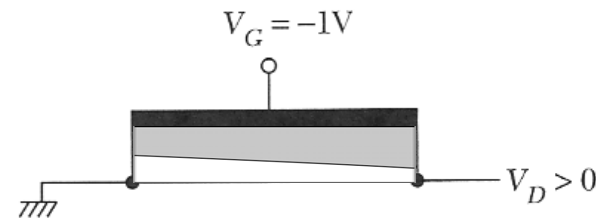
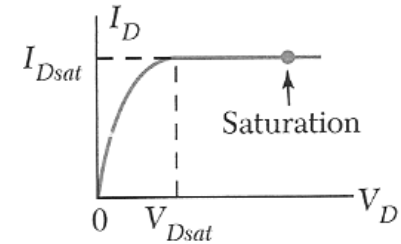
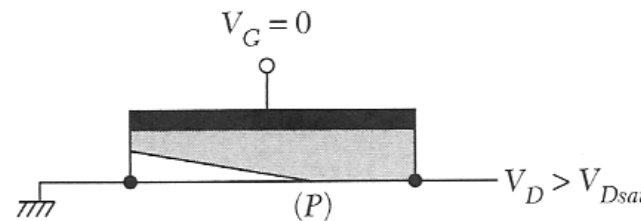
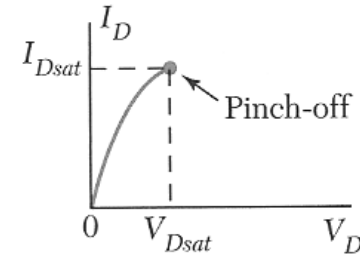
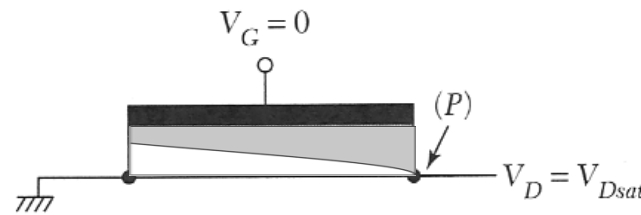
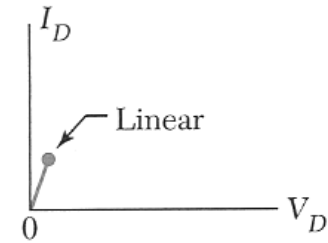
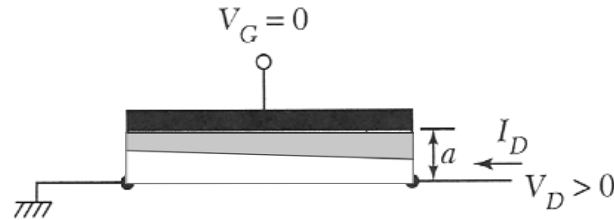
Pinch-off at  $h = x_n$

At Pinch-off, 
$$V = V_{bi} - \frac{eN_D h^2}{2\epsilon}$$

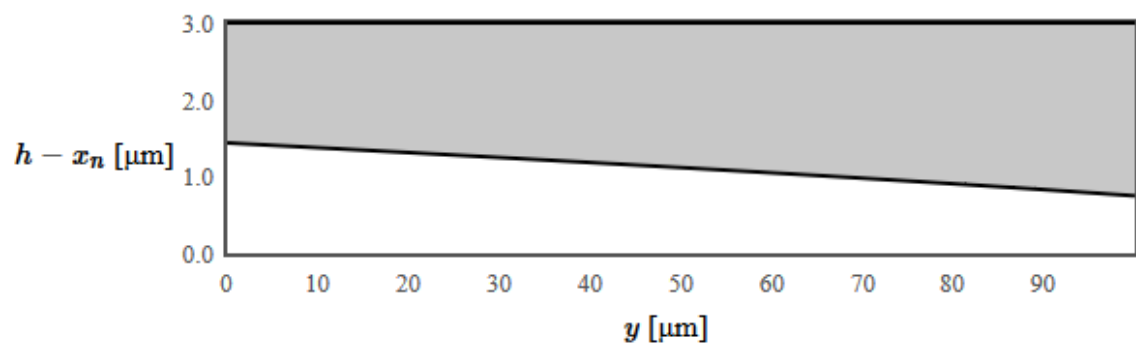
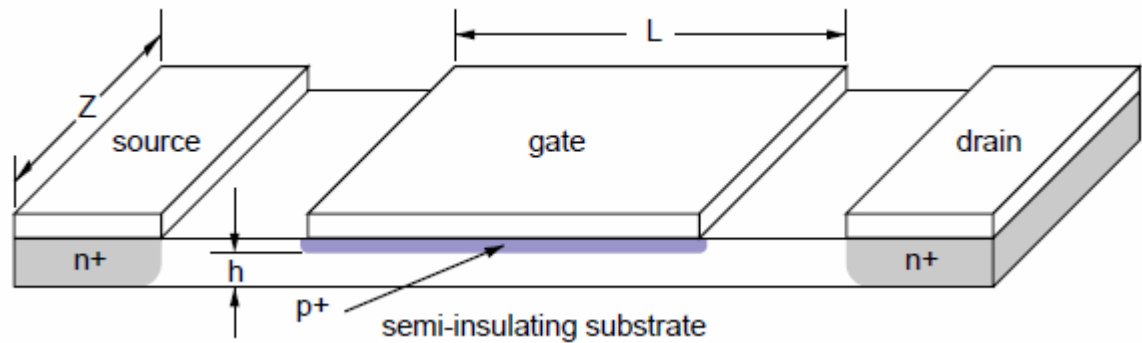


# JFET

The drain is the side of the transistor that gets pinched off.



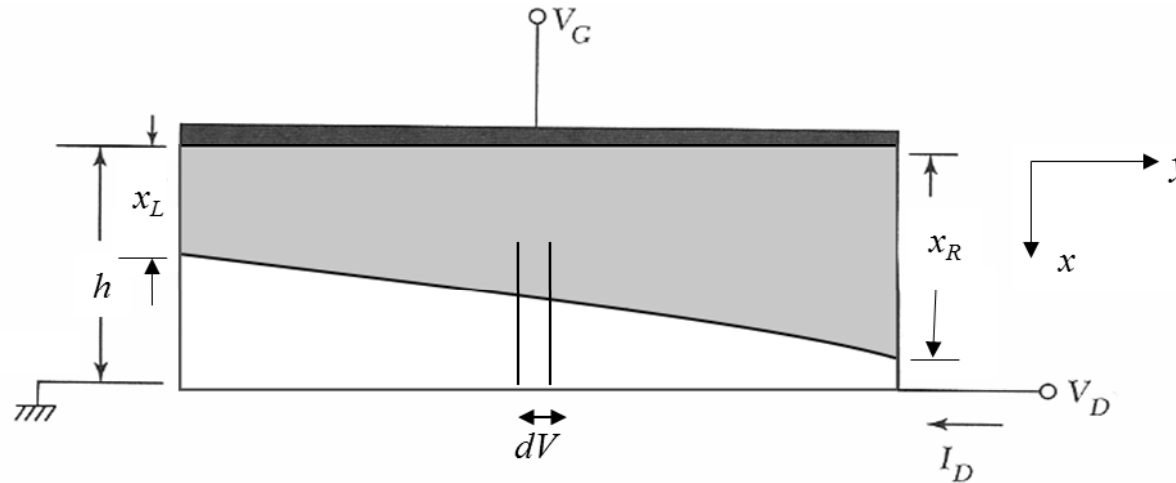
# JFET Gradual Channel Approximation



$N_c(300K) = 2.78E19 \text{ cm}^{-3}$   
 $N_v(300K) = 9.84E18 \text{ cm}^{-3}$   
 $E_g = 1.166 - 4.73E-4 * T * T / (T + 636) \text{ eV}$   
 $N_D = 1E15 \text{ cm}^{-3}$   
 $N_A = 1E19 \text{ cm}^{-3}$   
 $\mu_n = 1350 \text{ cm}^2/Vs$   
 $h = 3 \text{ }\mu\text{m}$   
 $L = 100 \text{ }\mu\text{m}$   
 $Z = 100 \text{ }\mu\text{m}$   
 $\epsilon_r = 11.9$   
 $T = 300 \text{ K}$   
 $V_D = 2 \text{ V}$   
 $V_g = -1 \text{ V}$

# JFET

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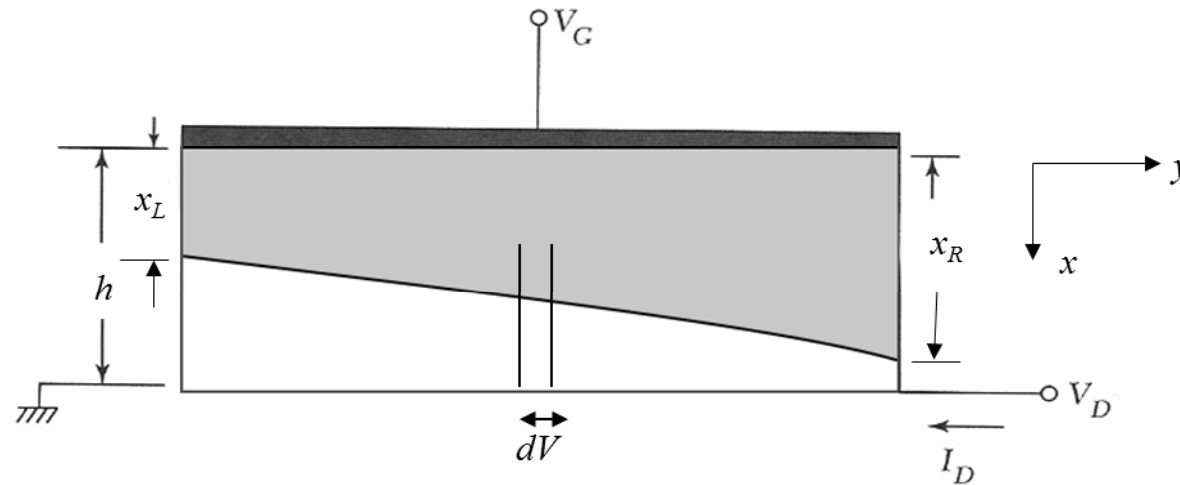


There is a long derivation to determine how the current depends on  $V_G$  and  $V_D$ .

We will find a relatively simple formula (probably familiar to electrical engineers).

Understanding the derivation is important for knowing when this formula is valid.

# JFET



$$dV = I_D dR = I_D \frac{\rho dy}{Z(h - x_n(y))}$$

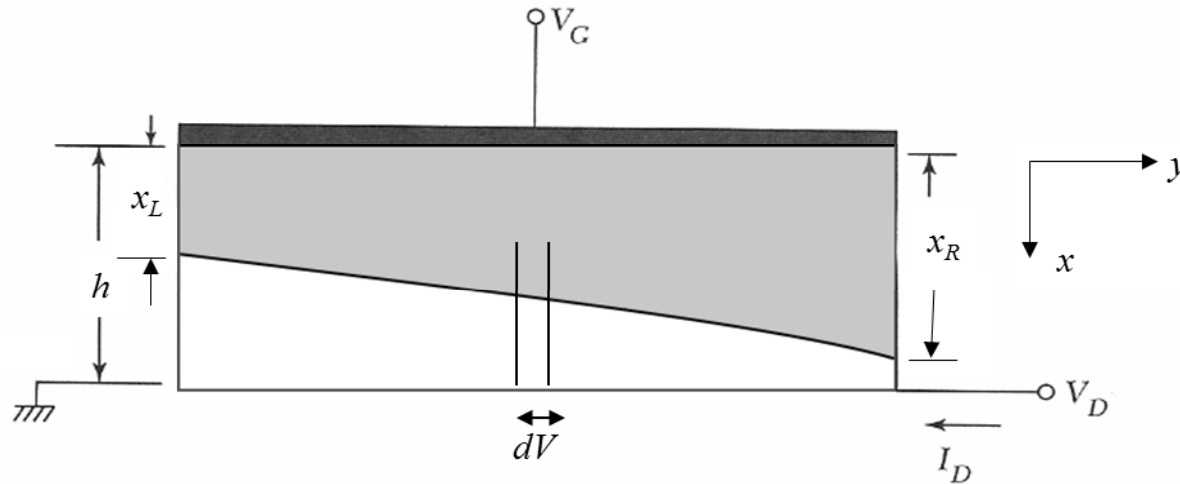
$$\rho = \frac{1}{\sigma} = \frac{1}{ne\mu_n} = \frac{1}{N_D e \mu_n}$$

$$dV = I_D \frac{dy}{e\mu_n N_D Z(h - x_n(y))}$$

$I_D$  is constant throughout the transistor  $\longrightarrow I_D dy = e\mu_n N_D Z(h - x_n(y)) dV$

# JFET

$$I_D dy = e \mu_n N_D Z (h - x_n(y)) dV$$



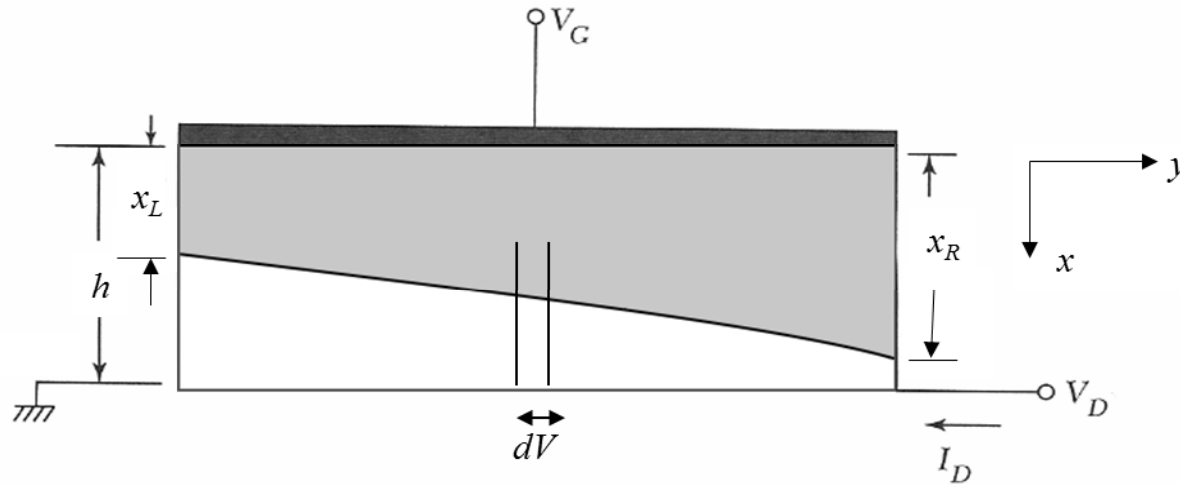
$V_G$  is a forward bias  
 $V(y)$  is a reverse bias.

depletion width is a function of position  $x_n(y) = \sqrt{\frac{2\epsilon(V_{bi} + V(y) - V_G)}{eN_D}}$

differentiate  $\frac{dx_n(y)}{dV} =$

# JFET

$$I_D dy = e\mu_n N_D Z (h - x_n(y)) dV$$



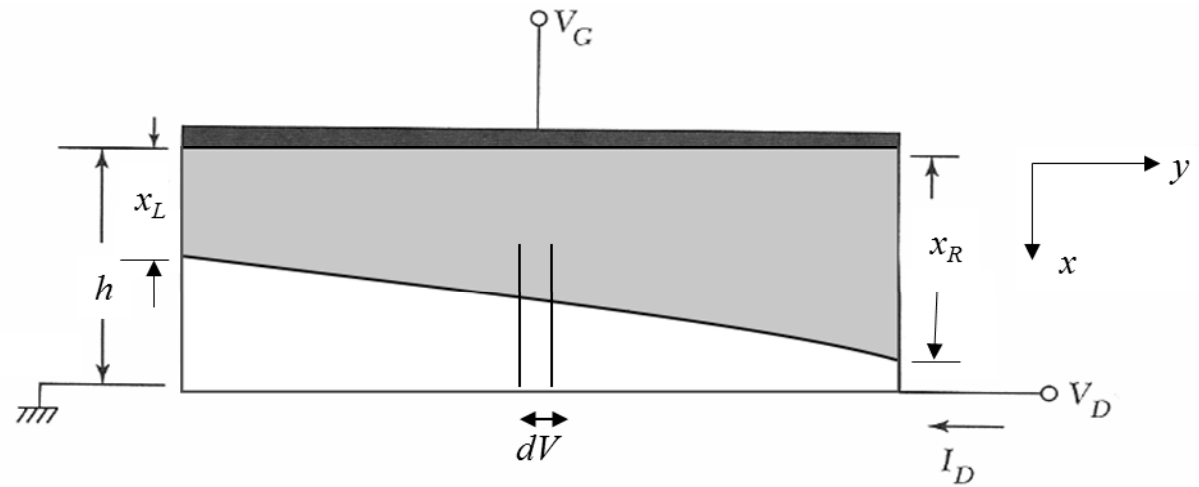
$V_G$  is a forward bias  
 $V(y)$  is a reverse bias.

depletion width is a function of position  $x_n(y) = \sqrt{\frac{2\epsilon(V_{bi} + V(y) - V_G)}{eN_D}}$

differentiate  $\frac{dx_n(y)}{dV} = \frac{1}{2} \left( \frac{2\epsilon(V_{bi} + V(y) - V_G)}{eN_D} \right)^{-1/2} \frac{2\epsilon}{eN_D} = \frac{\epsilon}{eN_D x_n(y)}$

$$dV = \frac{eN_D x_n}{\epsilon} dx_n$$

# JFET



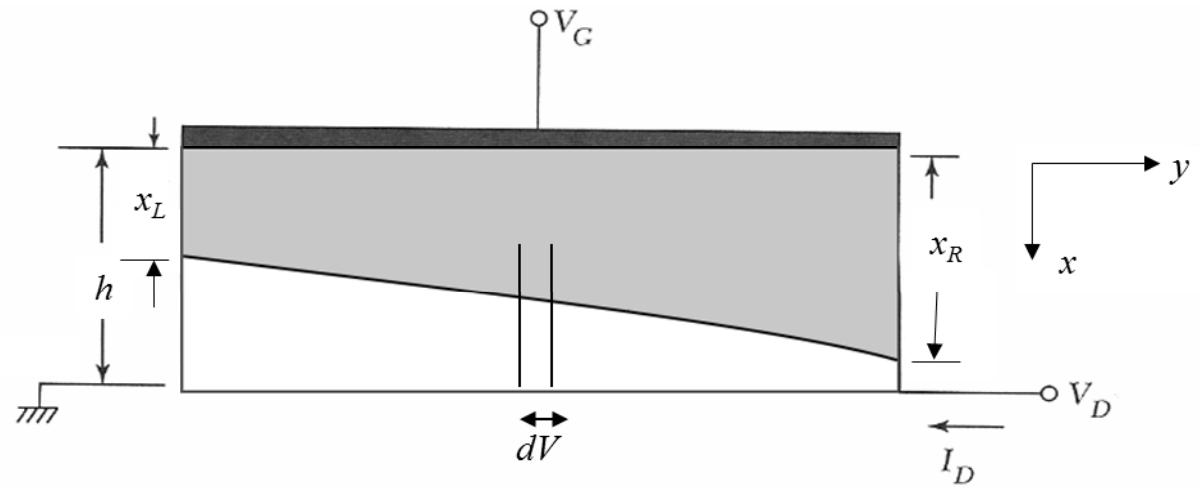
$$I_D dy = e\mu_n N_D Z (h - x_n(y)) dV \quad \leftarrow \text{from last slide}$$

$$dV = \frac{eN_D x_n}{\epsilon} dx_n$$

$$\frac{dx_n(y)}{dy} = \frac{I_D}{e\mu_n N_D Z (h - x_n(y)) \frac{eN_D}{\epsilon} x_n(y)}$$

If  $I_D$  is known, this can be solved for  $x_n(y)$ .

# JFET



$$I_D dy = e\mu_n N_D Z (h - x_n(y)) dV \quad \leftarrow \text{from a previous slide}$$

$$dV = \frac{eN_D x_n}{\epsilon} dx_n \quad \leftarrow \text{from a previous slide}$$

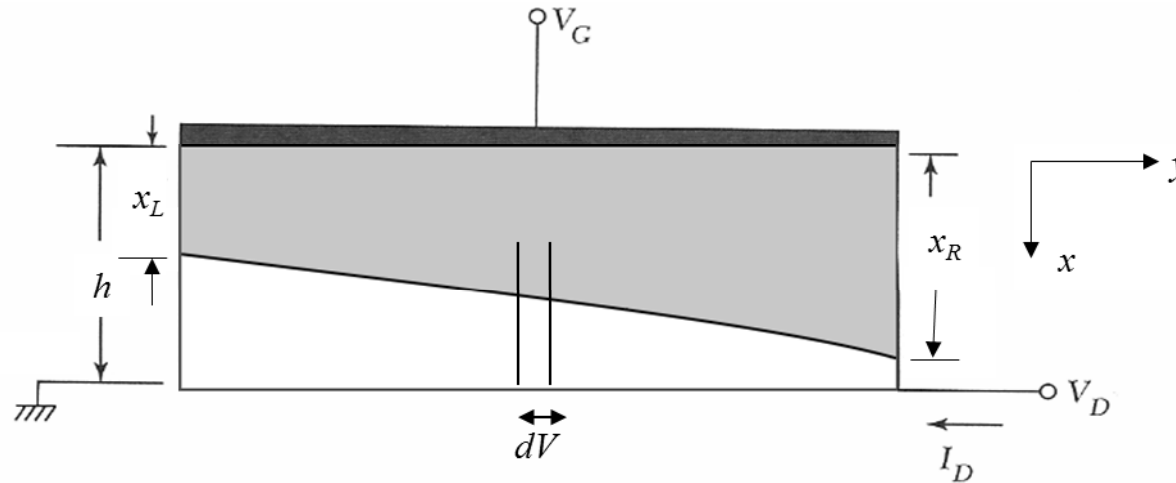
$$I_D dy = e\mu_n N_D Z (h - x_n(y)) \frac{eN_D}{\epsilon} x_n dx_n$$

$$I_D \int_0^L dy = e\mu_n N_D Z \frac{eN_D}{\epsilon} \int_{x_L}^{x_R} (h - x_n(y)) x_n dx_n$$

$$I_D = \frac{\mu_n N_D^2 Z e^2}{2L\epsilon} \left[ h(x_R^2 - x_L^2) - \frac{2}{3}(x_R^3 - x_L^3) \right]$$



# JFET



$$I_D = \frac{\mu_n N_D^2 Z e^2}{2L\epsilon} \left[ h(x_R^2 - x_L^2) - \frac{2}{3}(x_R^3 - x_L^3) \right]$$

$$h = \sqrt{\frac{2\epsilon V_p}{eN_D}}$$

$$x_L = \sqrt{\frac{2\epsilon(V_{bi} - V_G)}{eN_D}}$$

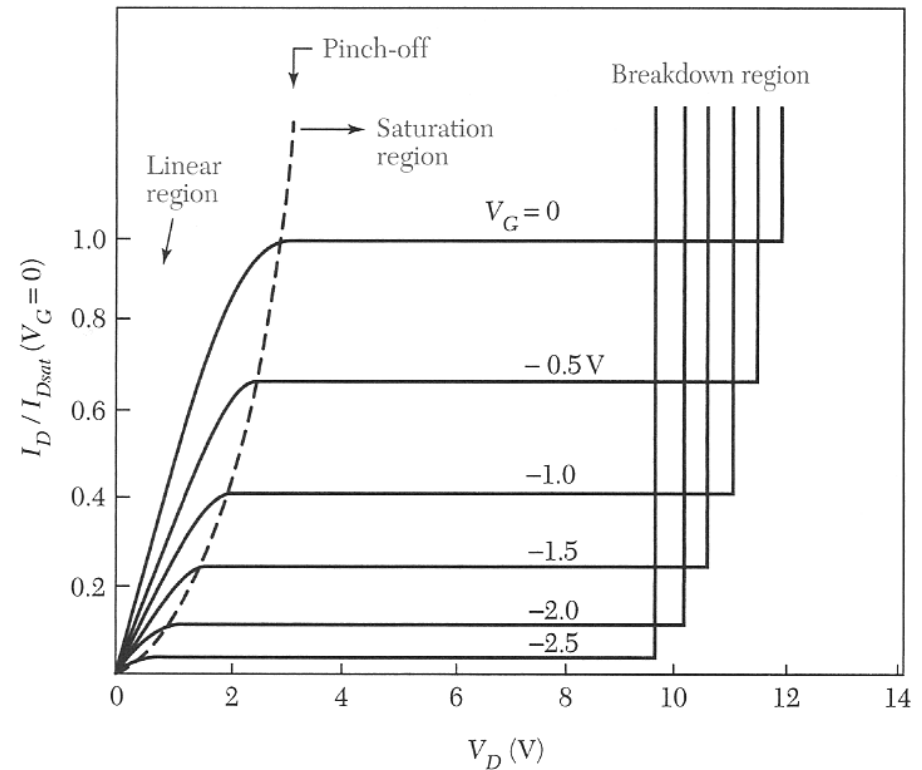
$$x_R = \sqrt{\frac{2\epsilon(V_{bi} - V_G + V_D)}{eN_D}}$$

# JFET - drain current

$$I_D = I_p \left[ \frac{V_D}{V_p} - \frac{2}{3} \left( \frac{V_{bi} + V_D - V_G}{V_p} \right)^{3/2} + \frac{2}{3} \left( \frac{V_{bi} - V_G}{V_p} \right)^{3/2} \right]$$

$$I_p = \frac{\mu_n N_D^2 Z e^2 h^3}{2L\epsilon}$$

valid in the linear regime  
(until pinch-off)



# JFET - Linear regime

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$$I_D = I_p \left[ \frac{V_D}{V_p} - \frac{2}{3} \left( \frac{V_{bi} + V_D - V_G}{V_p} \right)^{3/2} + \frac{2}{3} \left( \frac{V_{bi} - V_G}{V_p} \right)^{3/2} \right]$$

In the linear regime  $V_D \ll V_{sat}$ .

$$\frac{dI_D}{dV_D} =$$

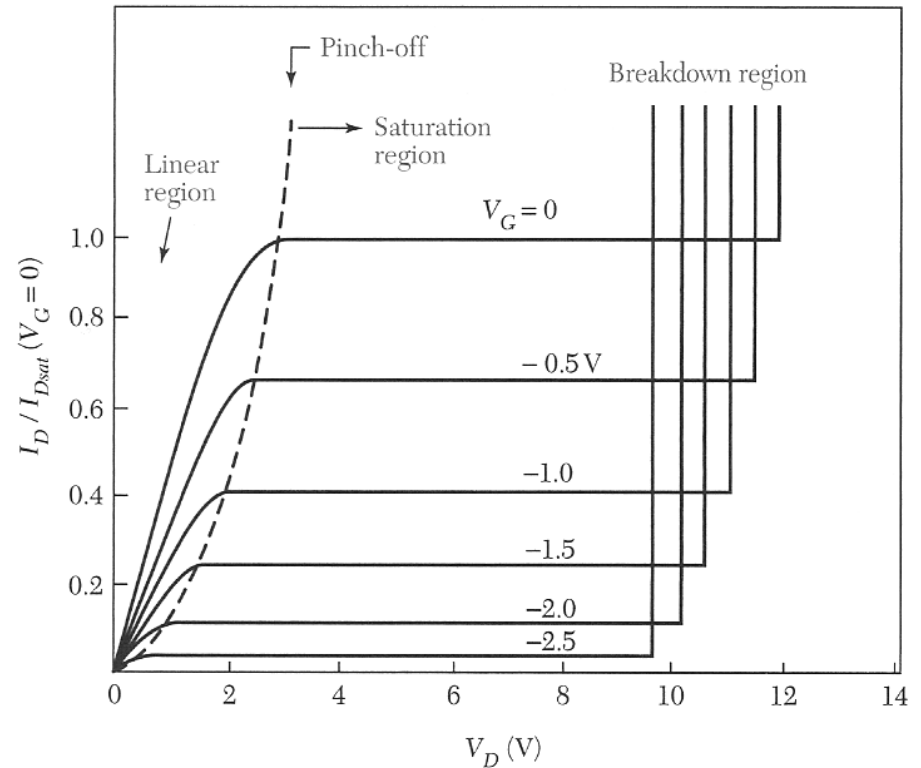
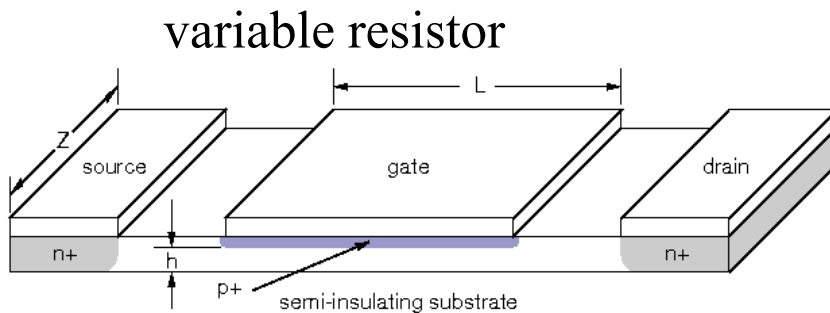
# JFET - Linear regime

$$I_D = I_p \left[ \frac{V_D}{V_p} - \frac{2}{3} \left( \frac{V_{bi} + V_D - V_G}{V_p} \right)^{3/2} + \frac{2}{3} \left( \frac{V_{bi} - V_G}{V_p} \right)^{3/2} \right]$$

In the linear regime  $V_D \ll V_{sat}$ .

$$\frac{dI_D}{dV_D} = I_p \left[ \frac{1}{V_p} - \frac{1}{V_p} \left( \frac{V_{bi} + V_D - V_G}{V_p} \right)^{1/2} \right]$$

$$I_D = \frac{I_p}{V_p} \left[ 1 - \sqrt{\frac{V_{bi} - V_G}{V_p}} \right] V_D \text{ for } V_D \ll V_{sat}$$



# JFET - Saturation regime

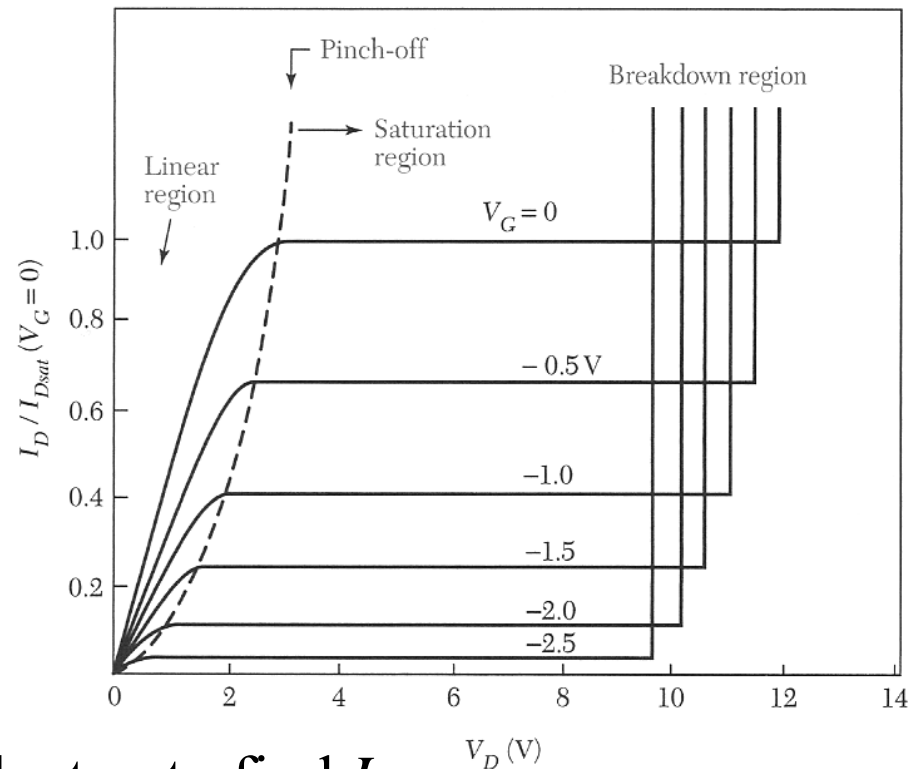
$$I_D = I_p \left[ \frac{V_D}{V_p} - \frac{2}{3} \left( \frac{V_{bi} + V_D - V_G}{V_p} \right)^{3/2} + \frac{2}{3} \left( \frac{V_{bi} - V_G}{V_p} \right)^{3/2} \right]$$

set  $dI_D/dV_D = 0$  to find  $V_{sat}$

$$\frac{dI_D}{dV_D} = I_p \left[ \frac{1}{V_p} - \frac{1}{V_p} \left( \frac{V_{bi} + V_D - V_G}{V_p} \right)^{1/2} \right] = 0$$

$$dI_D/dV_D = 0 \text{ when } \frac{V_{bi} + V_D - V_G}{V_p} = 1$$

$$V_{sat} = V_p - V_{bi} + V_G$$



Substitute  $V_{sat}$  into the equation at the top to find  $I_{sat}$

# JFET - Saturation regime

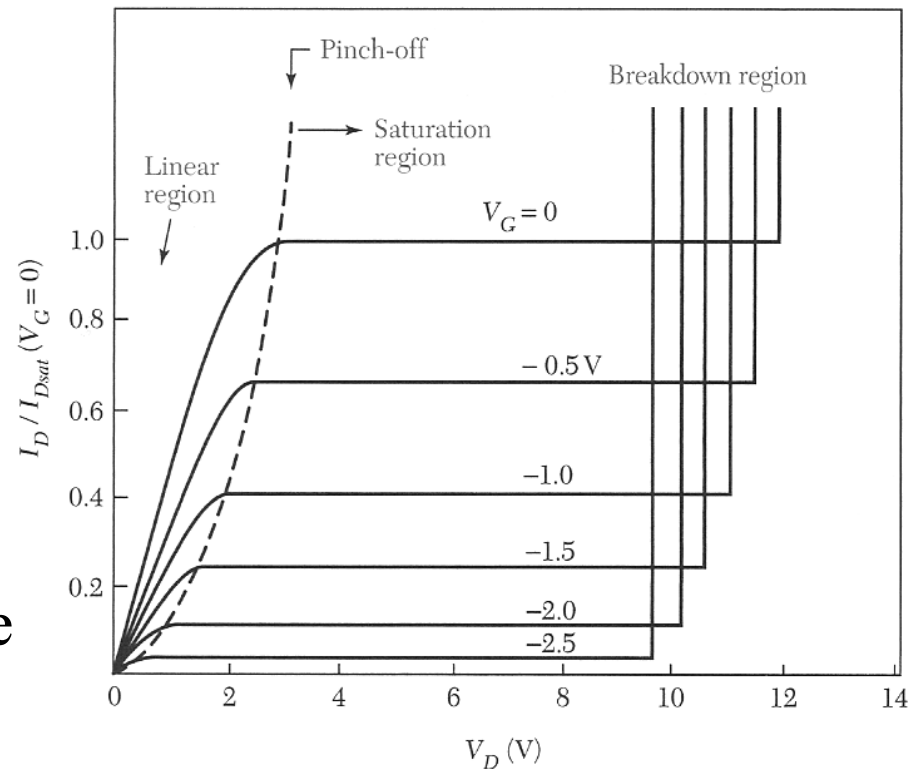
$$V_{sat} = V_p - V_{bi} + V_G$$

$$I_D = I_p \left[ \frac{V_D}{V_p} - \frac{2}{3} \left( \frac{V_{bi} + V_D - V_G}{V_p} \right)^{3/2} + \frac{2}{3} \left( \frac{V_{bi} - V_G}{V_p} \right)^{3/2} \right]$$

$$I_{sat} = I_p \left[ \frac{1}{3} - \frac{V_{bi} - V_G}{V_p} + \frac{2}{3} \left( \frac{V_{bi} - V_G}{V_p} \right)^{3/2} \right]$$

No  $V_D$  dependence

Voltage controlled current source



# JFET - transconductance

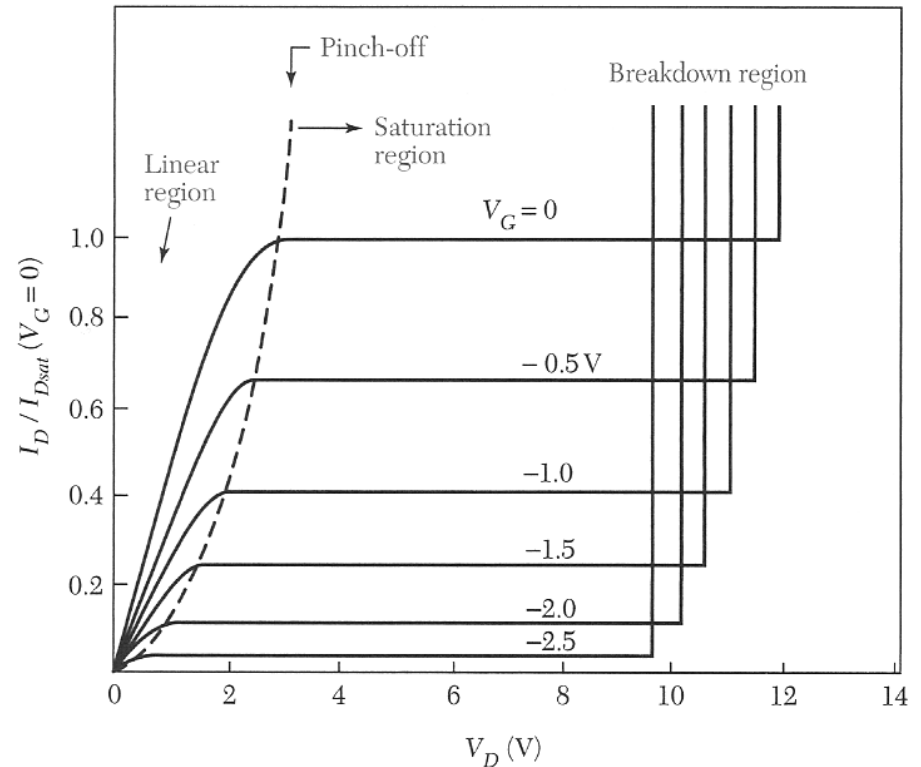
In the saturation regime,

$$I_{sat} = I_p \left[ \frac{1}{3} - \frac{V_{bi} - V_G}{V_p} + \frac{2}{3} \left( \frac{V_{bi} - V_G}{V_p} \right)^{3/2} \right]$$

transconductance (describes how good the voltage controlled current source is)

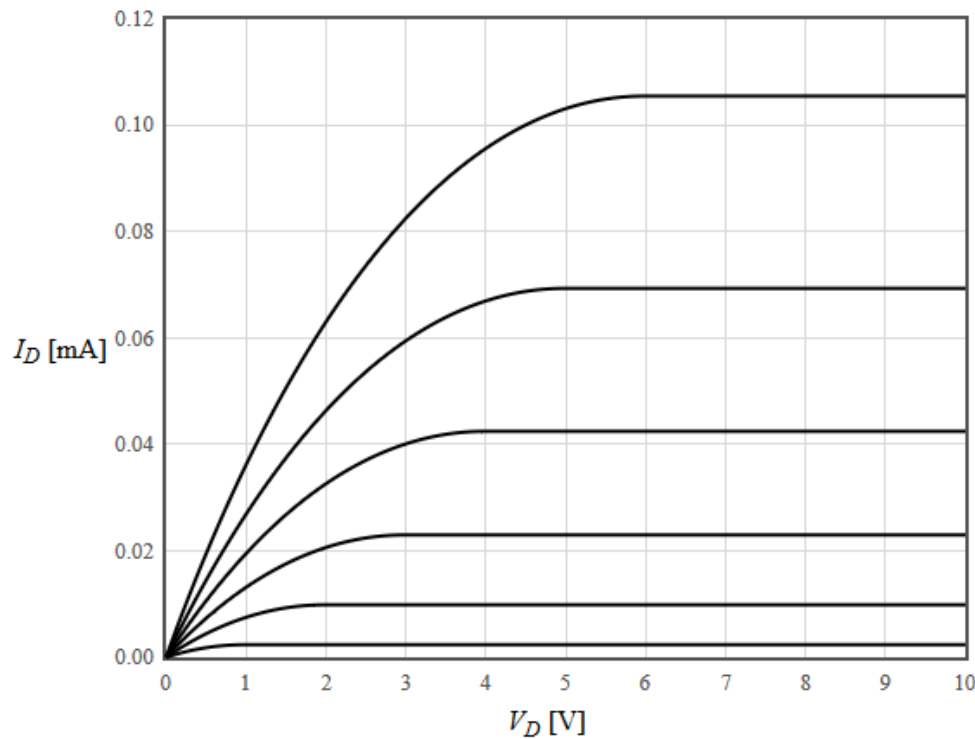
$$g_m = \frac{dI_{sat}}{dV_G} = \frac{I_p}{V_p} \left( 1 - \sqrt{\frac{V_{bi} - V_G}{V_p}} \right)$$

$$g_m = \frac{dI_{sat}}{dV_G} = \frac{2Z\mu_n e N_D h}{L} \left( 1 - \sqrt{\frac{V_{bi} - V_G}{V_p}} \right)$$



# JFET

$$I_D = I_p \left[ \frac{V_D}{V_p} - \frac{2}{3} \left( \frac{V_{bi} + V_D - V_G}{V_p} \right)^{3/2} + \frac{2}{3} \left( \frac{V_{bi} - V_G}{V_p} \right)^{3/2} \right]$$



$N_c(300K)$	2.78E19	cm <sup>-3</sup>
$N_v(300K)$	9.84E18	cm <sup>-3</sup>
$E_g$	1.166-4.73E-4*T*T/(T+636)	eV
$N_D$	1E15	cm <sup>-3</sup>
$N_A$	1E19	cm <sup>-3</sup>
$\mu_n$	1350	cm <sup>2</sup> /Vs
$h$	3	$\mu$ m
$L$	100	$\mu$ m
$Z$	100	$\mu$ m
$\epsilon_r$	11.9	
$T$	300	K
$V_D(\text{max})$	10	V
$V_g$ [1]	0	V
$V_g$ [2]	-1	V
$V_g$ [3]	-2	V
$V_g$ [4]	-3	V
$V_g$ [5]	-4	V
$V_g$ [6]	-5	V

Replot

Si Ge GaAs

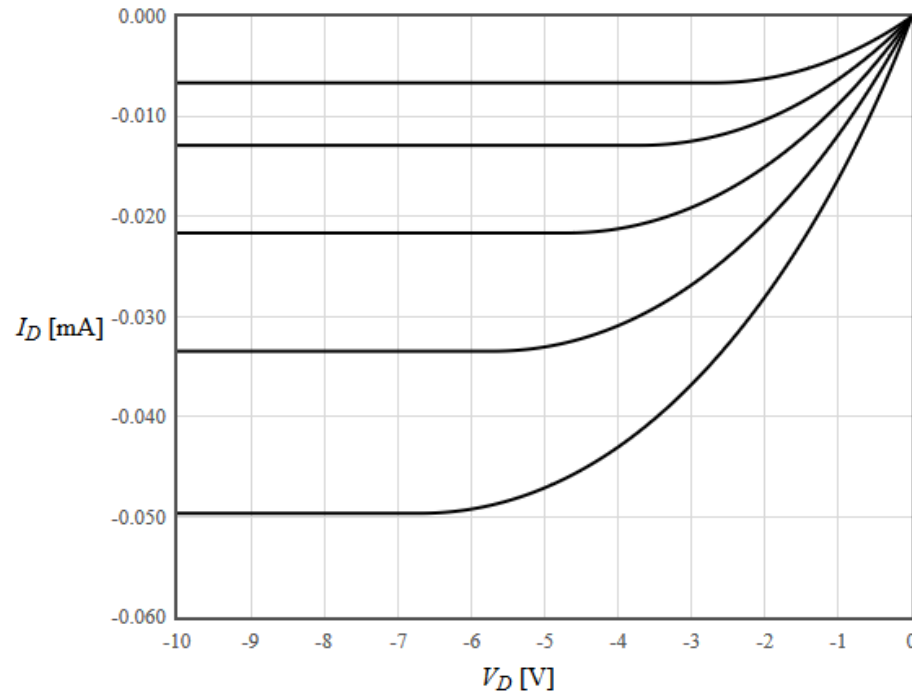
$$E_g = 1.12 \text{ eV}; \quad n_i = 6.41 \times 10^9 \text{ cm}^{-3}; \quad V_{bi} = 0.856 \text{ V}; \quad I_p = 0.000444 \text{ A}; \quad V_p = 6.84 \text{ V}.$$



# p-channel JFET

The expression for the drain current of a p-channel JFET in the linear regime is,

$$I_D = I_p \left[ \frac{V_D}{V_p} - \frac{2}{3} \left( \frac{V_{bi} + V_D - V_G}{V_p} \right)^{3/2} + \frac{2}{3} \left( \frac{V_{bi} - V_G}{V_p} \right)^{3/2} \right]$$



$N_c(300K)$	<input type="text" value="2.78E19"/>	cm <sup>-3</sup>
$N_v(300K)$	<input type="text" value="9.84E18"/>	cm <sup>-3</sup>
$E_g$	<input type="text" value="1.166-4.73E-4*T*T/(T+636)"/>	eV
$N_D$	<input type="text" value="1E19"/>	cm <sup>-3</sup>
$N_A$	<input type="text" value="1E15"/>	cm <sup>-3</sup>
$\mu_p$	<input type="text" value="480"/>	cm <sup>2</sup> /Vs
$h$	<input type="text" value="3"/>	μm
$L$	<input type="text" value="100"/>	μm
$Z$	<input type="text" value="100"/>	μm
$\epsilon_r$	<input type="text" value="11.9"/>	
$T$	<input type="text" value="300"/>	K
$V_D(\text{min})$	<input type="text" value="-10"/>	V
$V_g [1]$	<input type="text" value="0"/>	V
$V_g [2]$	<input type="text" value="1"/>	V
$V_g [3]$	<input type="text" value="2"/>	V
$V_g [4]$	<input type="text" value="3"/>	V
$V_g [5]$	<input type="text" value="4"/>	V
$V_g [6]$	<input type="text" value="5"/>	V

Replot

Si   Ge   GaAs

$E_g = 1.12 \text{ eV}; \quad n_i = 6.41 \times 10^9 \text{ cm}^{-3}; \quad V_{bi} = 0.856 \text{ V}; \quad I_p = -0.000158 \text{ A}; \quad V_p = -6.84 \text{ V}.$

# High frequencies

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$$\tilde{i}_{in} = 2\pi f C_G \tilde{v}_G$$

$$\tilde{i}_{out} = g_m \tilde{v}_G$$

for gain:  $\tilde{i}_{in} < \tilde{i}_{out}$

$$f < \frac{g_m}{2\pi C_G} = f_T$$

$f_T$  is the frequency  
where the gain drops  
below 1

average capacitance:

$$C_G = ZL \frac{\epsilon}{\bar{x}_n}$$

$$f_T = \frac{\mu_n e N_D h^2}{2\pi \epsilon L^2}$$

For velocity saturation, the approximation  $dV = I_D \frac{\rho dy}{Z(h - x_n(y))}$  is not valid

Ohm's law assumes  $v_d = \mu E$

$$f_T \approx \frac{v_s}{L}$$

